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	STATEME	NT UNDER 37 CFR 3.	73(b)	
Applicant	Patent Owner: Moreno, Miguel et al.			
Application No./Patent No.: 10/764919		Filed/Issue Date:	1/26/2004	
Entitled: U	Jitra Low Power Programmable Timer an	d Low Voltage Detection Circ	uits (DM-A673)	
	Microchip Technology Inc.		, a corporation	
(Na	ame of Assignee)	(Type of Assignee, e.g., corporati	on, partnership, university, government agency, etc.;	
states tha	t it is;			
1. 🔯	the assignee of the entire right, title, and interest; or			
2.	-	ssignee of less than the entire right, title, and interest in		
_	(The extent (by percentage) of its ownership interest is %); or			
3. 🔲	The assignee of an undivided interest in the entirety of (a complete assignment from one of the joint inventors was made)			
the paten	t application/patent identified above by vi	rtue of either:		
rec	assignment from the inventor(s) of the p orded in the United States Patent and Tr by thereof is attached.			
OR				
	chain of title from the inventor(s), of the pown below:	atent application/patent identi	fied above, to the current assignee as	
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	Additional documents in the chain of til	tle are listed on a supplement	al sheet(s).	
assignee [NOTI	quired by 37 CFR 3.73(b)(1)(i), the docur was, or concurrently is being, submitted £: A separate copy (i.e., a true copy of the Division in accordance with 37 CFR Part See MPEP 302.08]	for recordation pursuant to 37 ne original document(s)) must	CFR 3.11. be submitted to Assignment	
The unde	rsigned (whose title is supplied below) is	authorized to act on behalf o	f the assignee.	
	/Bruce W. Slayden II/		May 4, 2009	
	Signature		Date	
	Bruce W. Slayden II	Att	orney for the Applicant	
	Printed or Typed Name		Title	

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